

intended to illustrate, but not to limit the invention.

The drawings include the following Figures:

[0017] Figure 1A shows a host system with an adapter coupled to a storage subsystem, used according to one aspect of the present invention;

[0018] Figure 1B shows a block diagram with details of an adapter, used according to one aspect of the present invention;

[0019] Figure 1C shows the internal architecture of a host system, used according to one aspect of the present invention;

[0020] Figure 1D shows a top-level block diagram where two adapters are connected to a host system using a bridge;

[0021] Figures 2A and 2B show block diagrams where the same PCI-Exp connector is used to couple more than one adapter, according to one aspect of the present invention; and

Figures 2C and 2D show
[0022] ~~Figure 2C shows~~ a block diagram where the same PCI-Exp connector is used to couple plural adapters, according to one aspect of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0023] To facilitate an understanding of the preferred embodiment, the general architecture and operation of a system using storage devices will be described. The